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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Brian Doyle et al.

U.S. Serial No: 10/816,232

Filed: March 31, 2004

For: SEMICONDUCTOR DEVICE HAVING
A Laterally Modulated Gate
Work Function and Method of
Fabrication

Commissioner of Patents and Trademarks
P.O. Box 1405
Alexandria, VA 22313-1450

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) Examiner: Wojciechowicz, Edward J.
)
) Art Unit: 2815
)

I hereby certify that this correspondence is being deposited
with the United States Postal Service as first class mail with
sufficient postage in an envelope addressed to the Commissioner
for Patents, PO Box 1450, Alexandria, Virginia 22313-1450

on April 4, 2005
Date of Deposit
TERESA MATLOX
Name of Person Mailing Correspondence
Teresa Matlox 4/4/05
Signature Date

RESPONSE TO ELECTION REQUIREMENT

Sir:

In response to the Election Requirement mailed March 23, 2005, applicant(s) hereby
elects to prosecute Group I claims 1-11, drawn to a semiconductor device. As such, applicant(s)
elects to withdraw Group II claims 12-28, drawn to a process.

If there are any additional charges, please charge Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN

Date: 4/4, 2005

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